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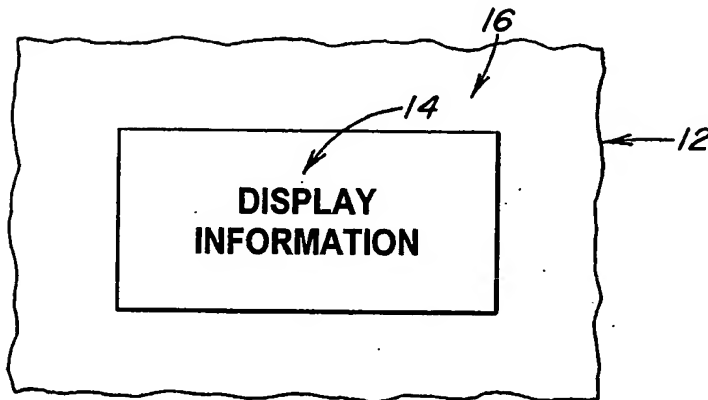


Fig. 1

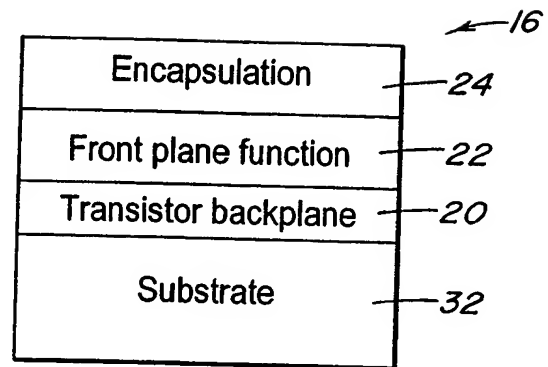


Fig. 2a

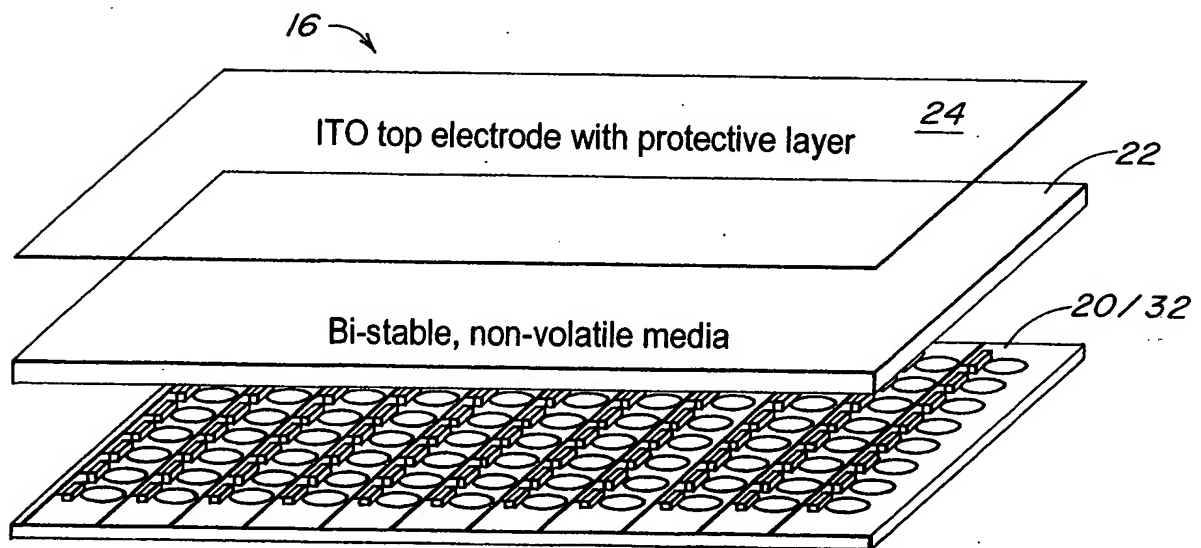


Fig. 2b

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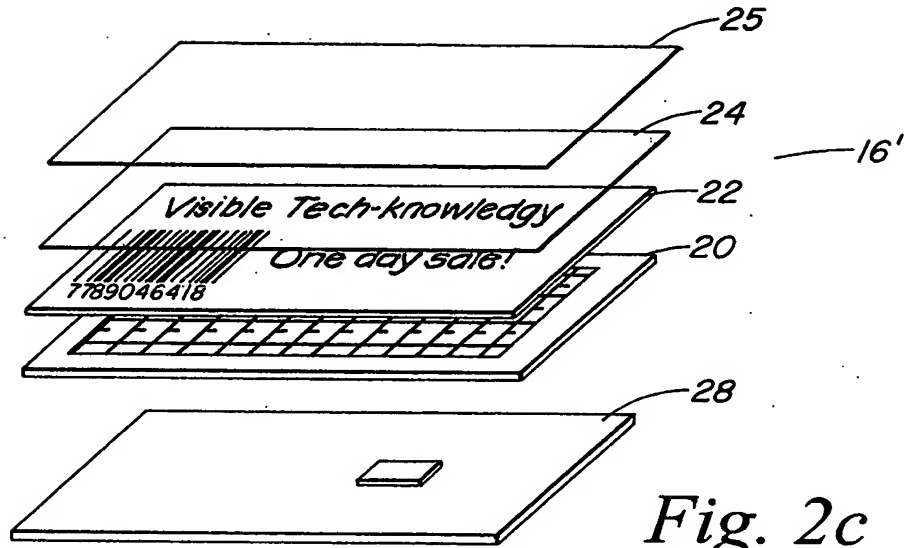


Fig. 2c

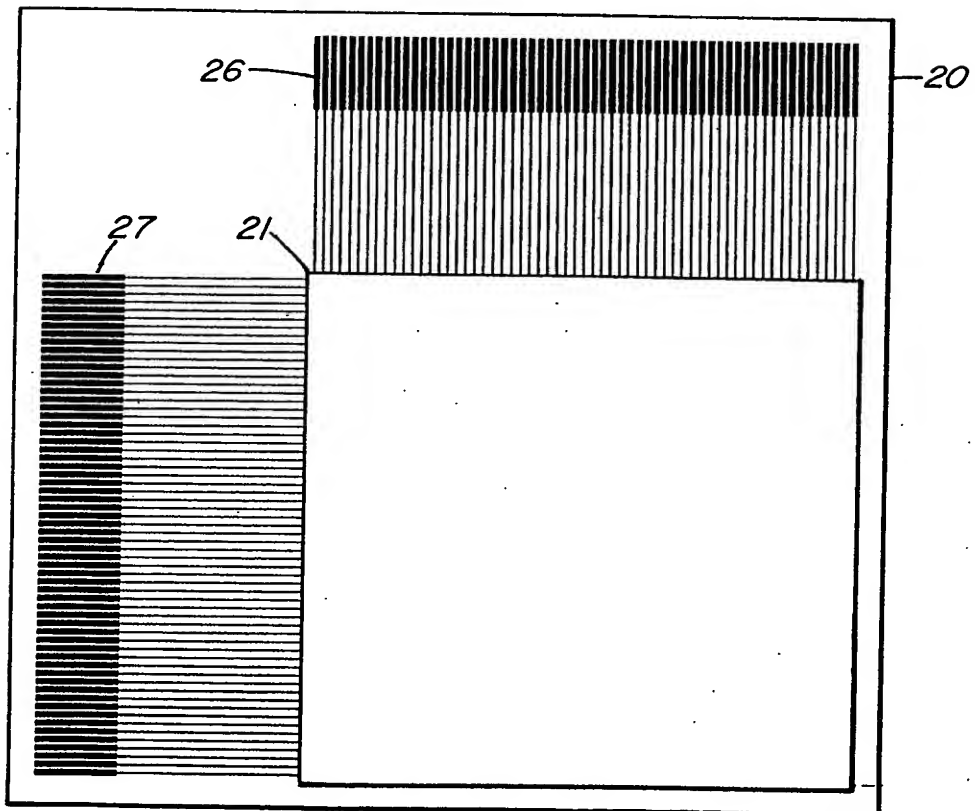


Fig. 3

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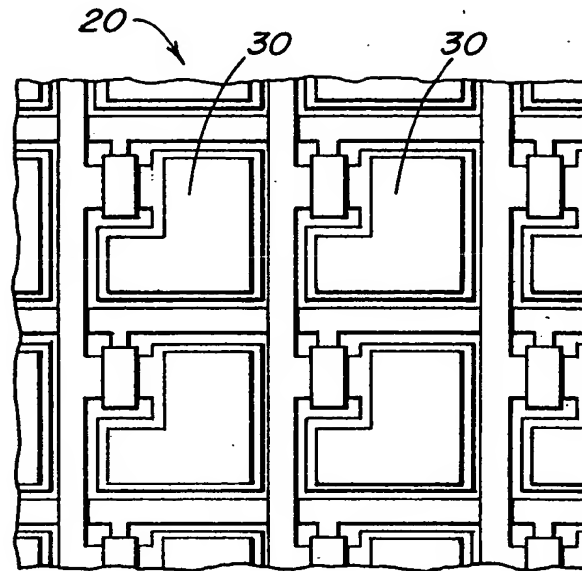


Fig. 4

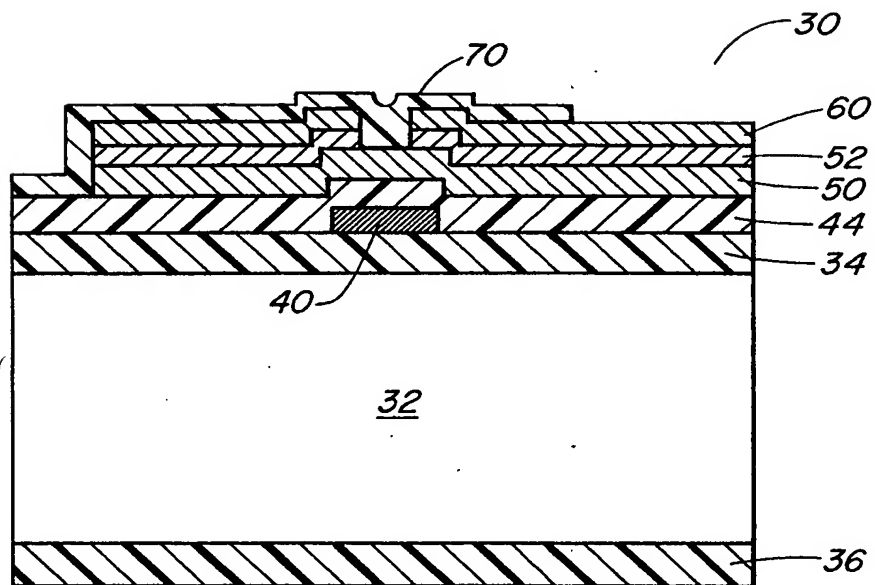


Fig. 5

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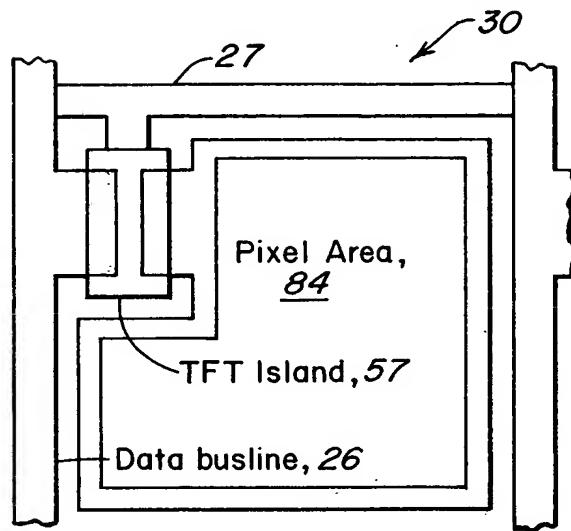


Fig. 6

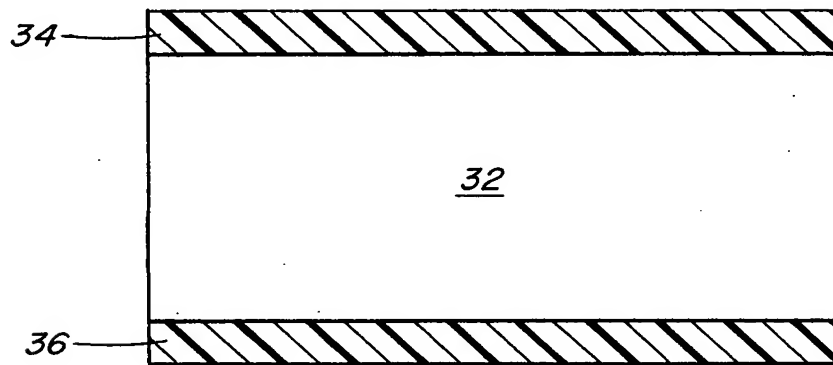


Fig. 7

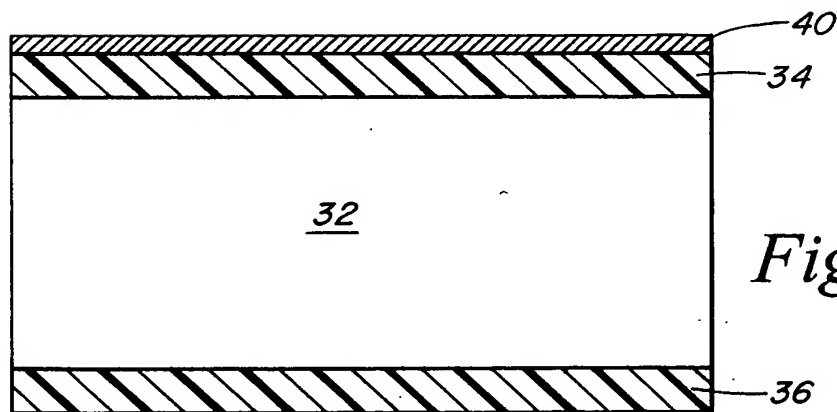


Fig. 8

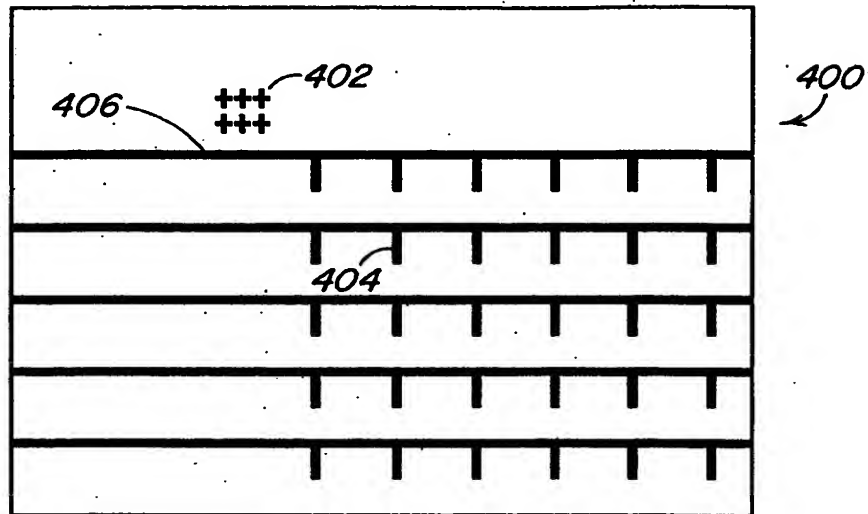


Fig. 9

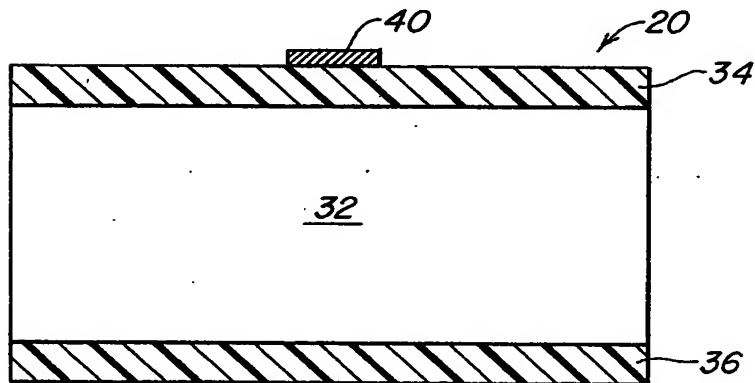


Fig. 10a

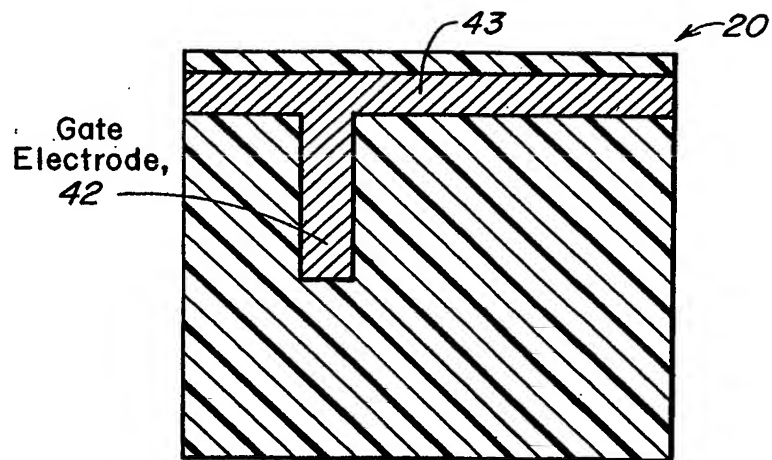


Fig. 10b

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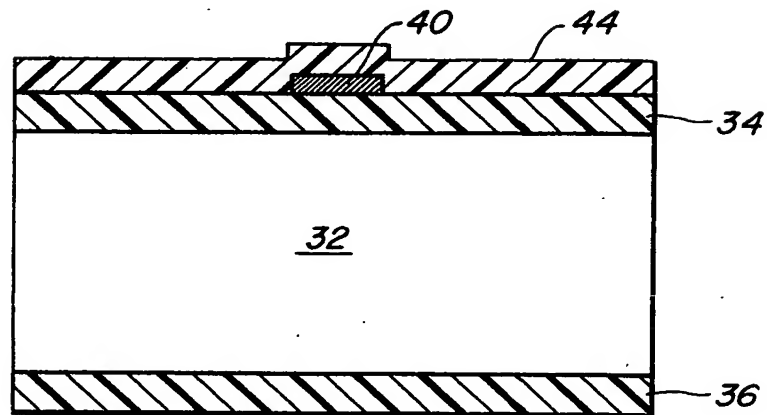


Fig. 11

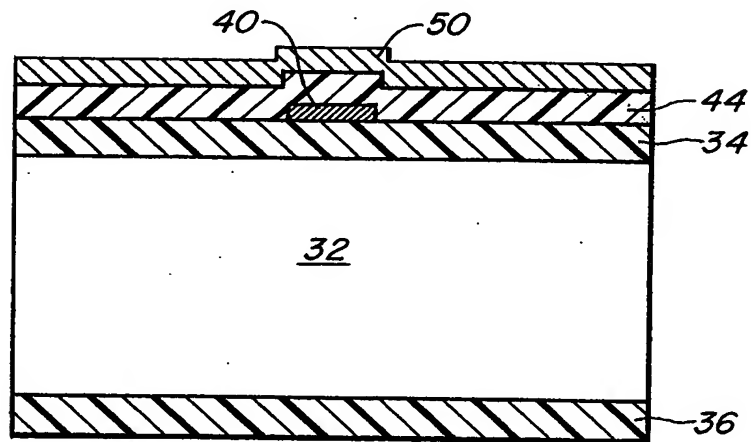


Fig. 12

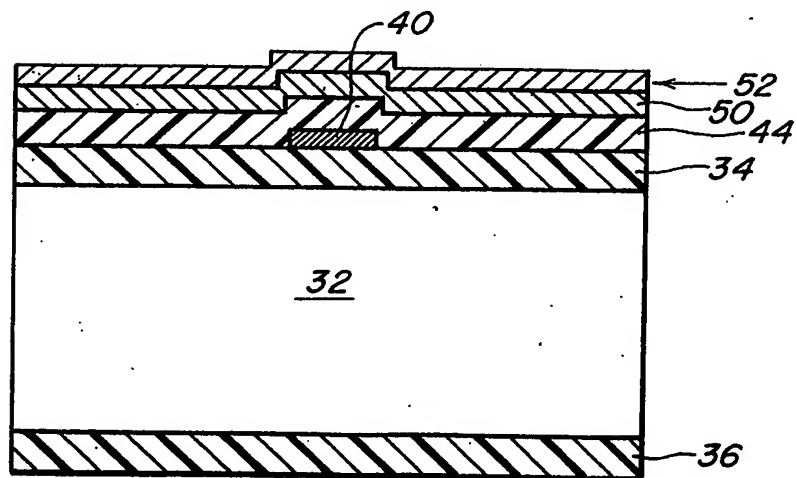


Fig. 13

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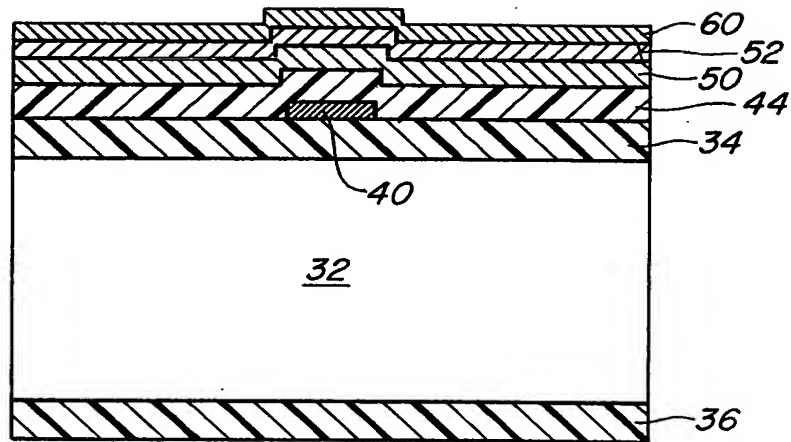


Fig. 14

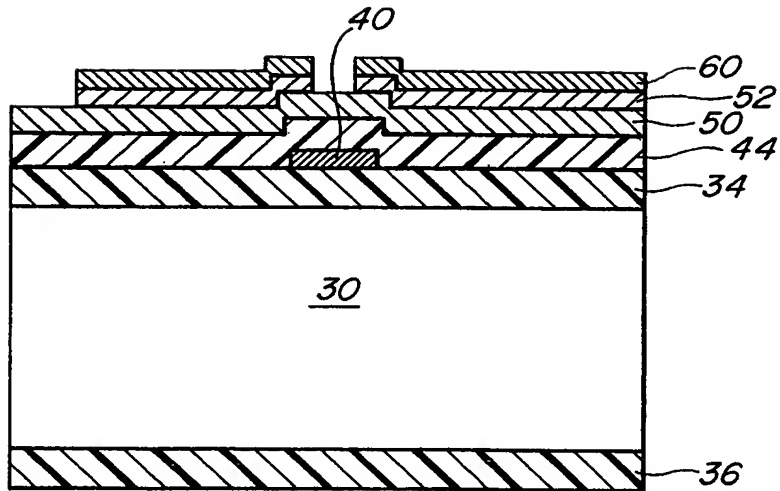


Fig. 15a

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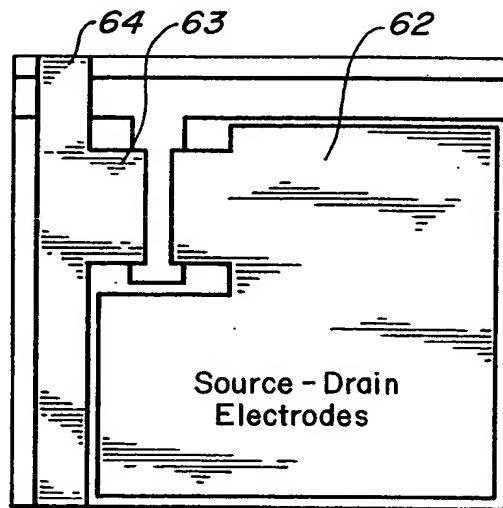


Fig. 15b

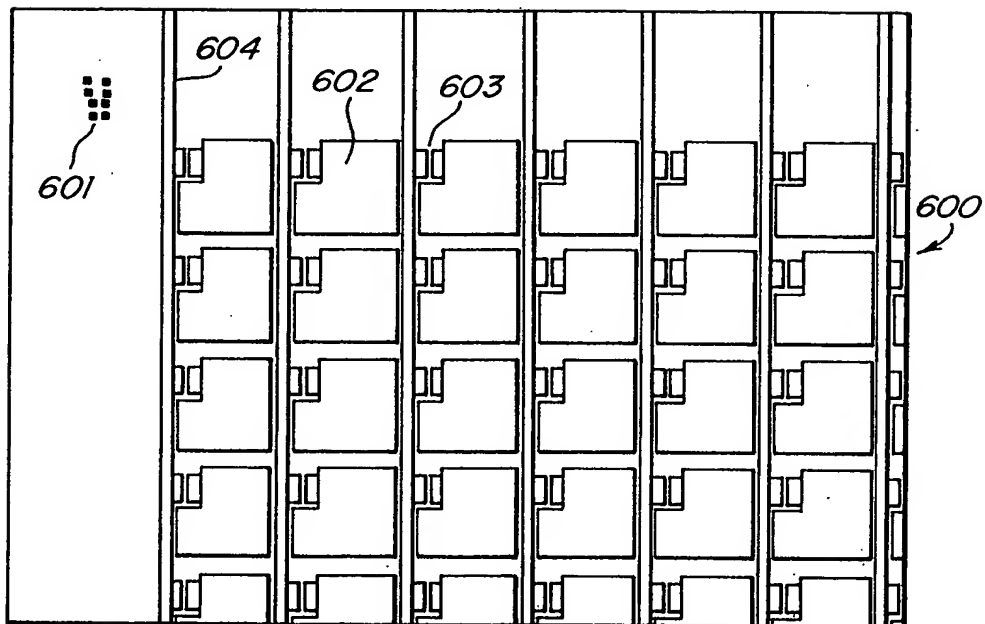


Fig. 16

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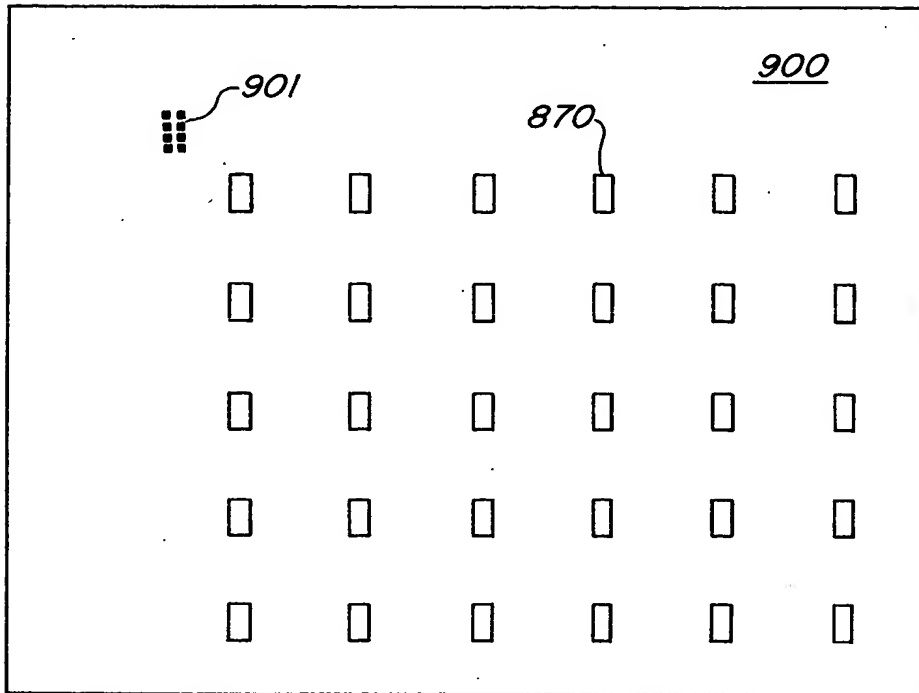


Fig. 17

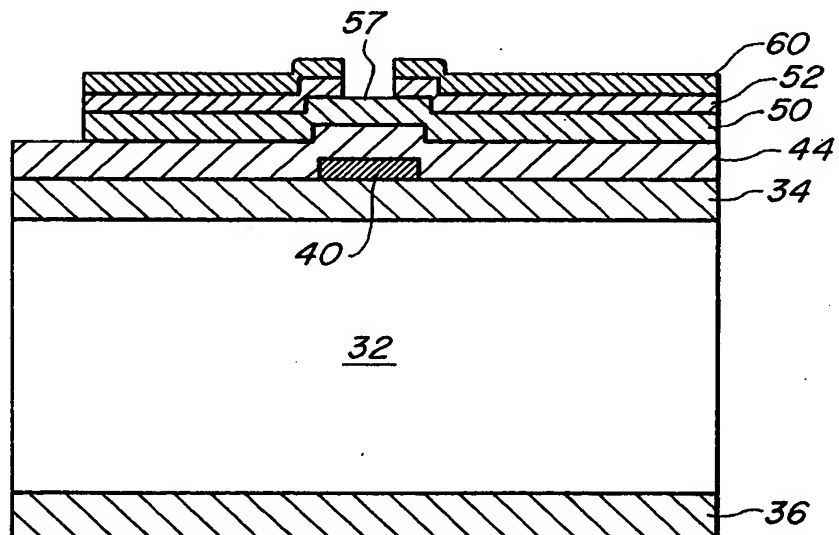
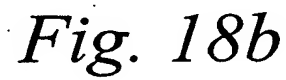


Fig. 18a



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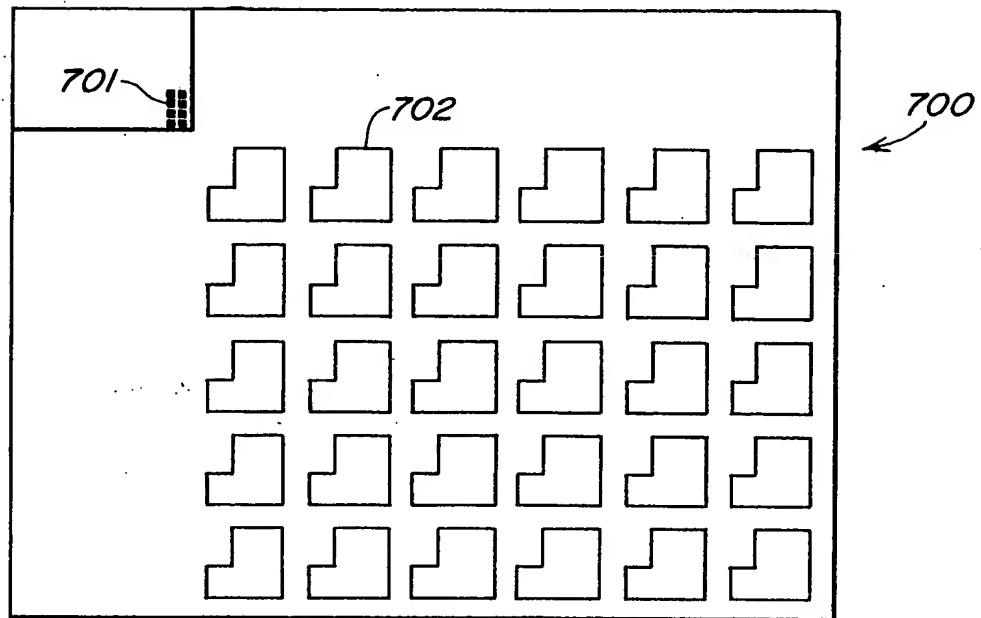


Fig. 20

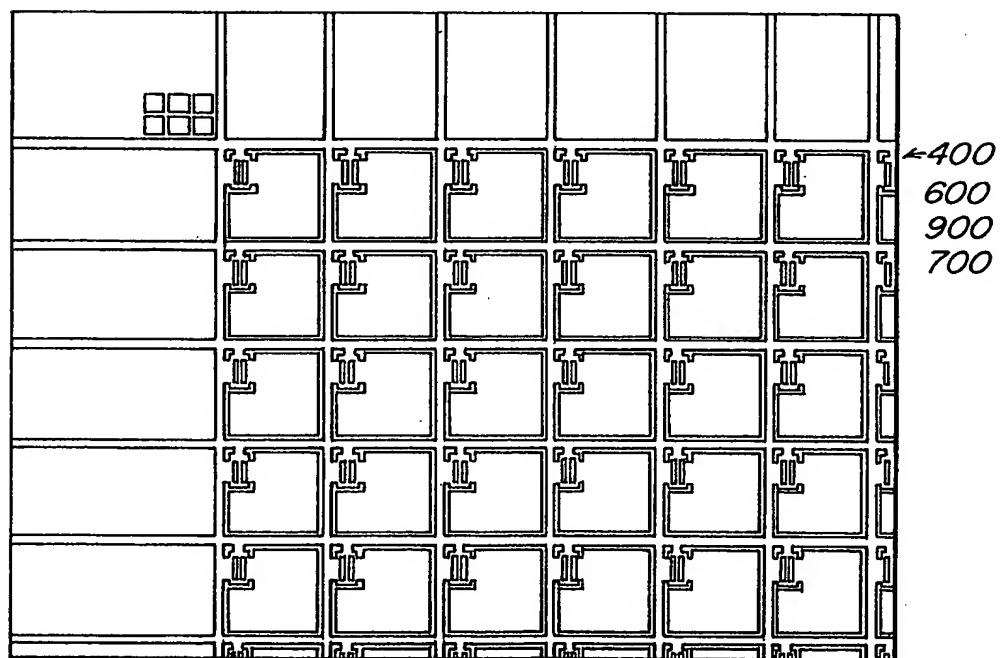


Fig. 21

App No.: Filed Herewith
Inventor: Forbes, et al.
Docket No.: VTW-010DV2
Title: Active Matrix Thin Film Transistor Array...

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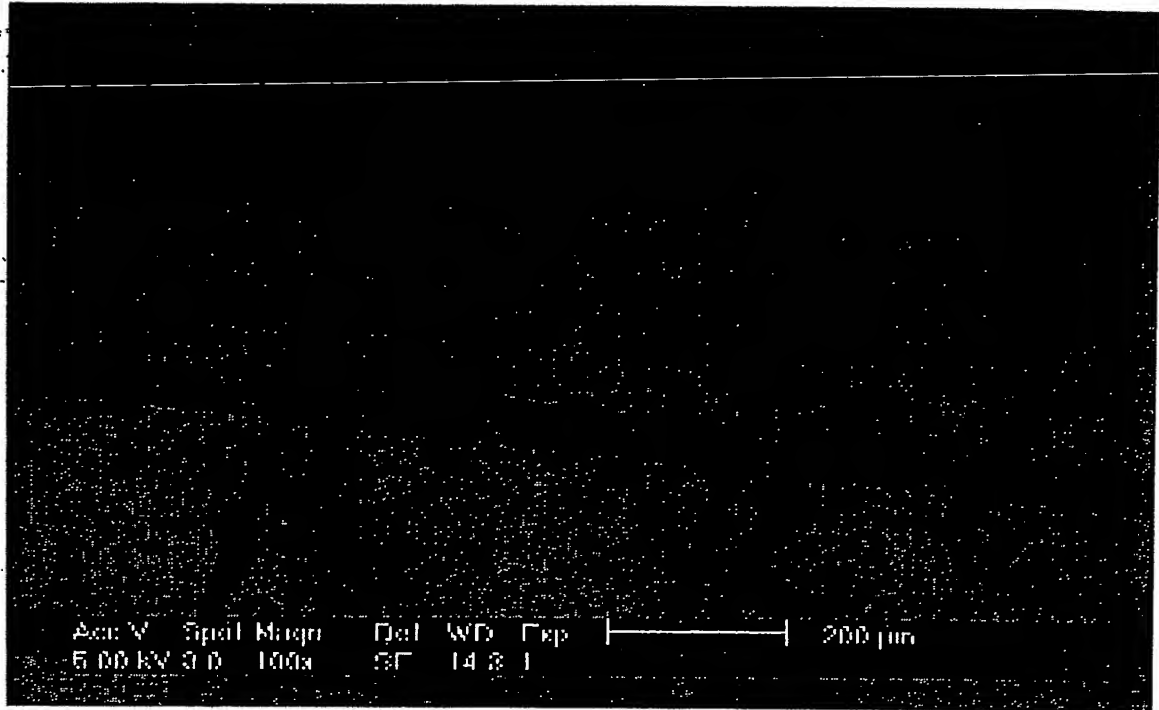


Fig. 22

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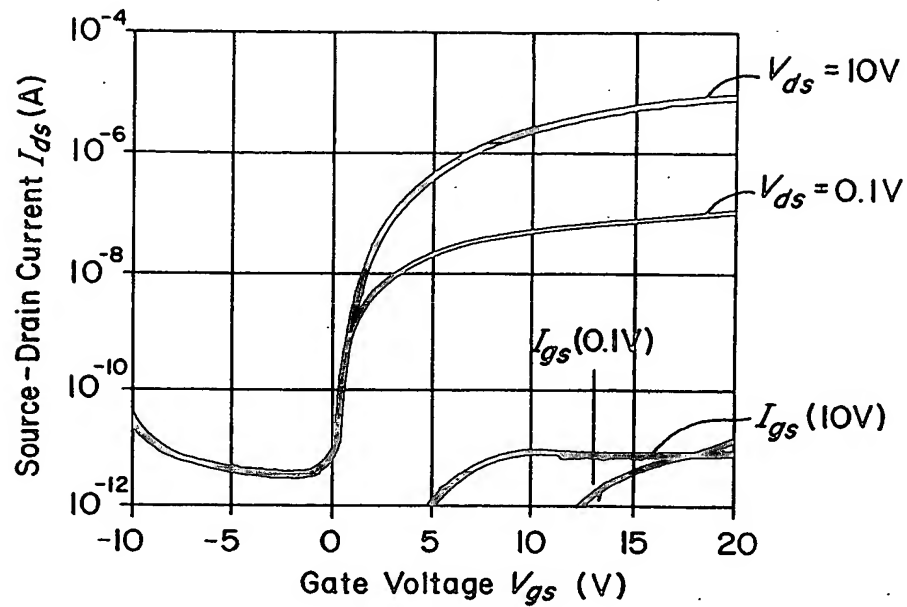


Fig. 23

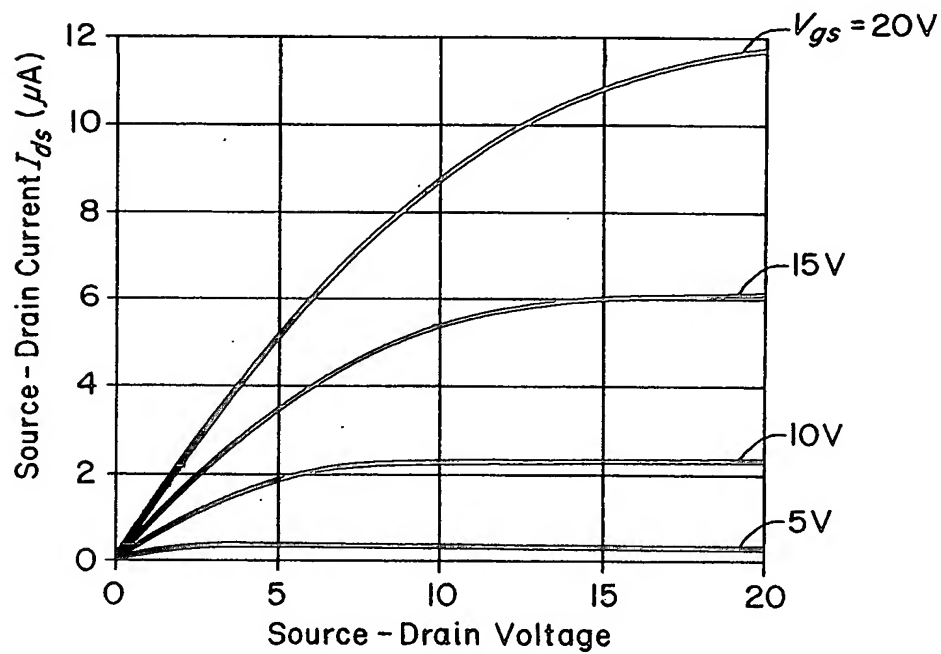


Fig. 24

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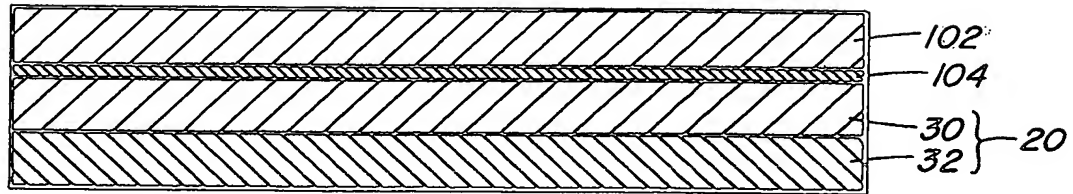


Fig. 25

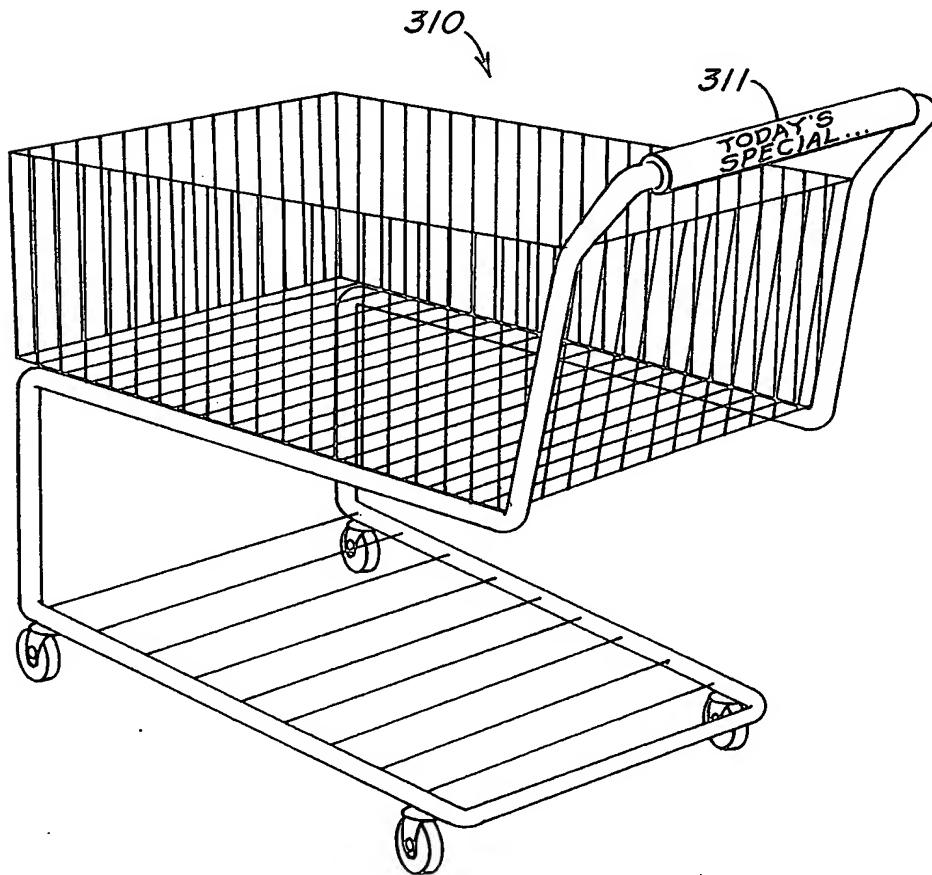


Fig. 26

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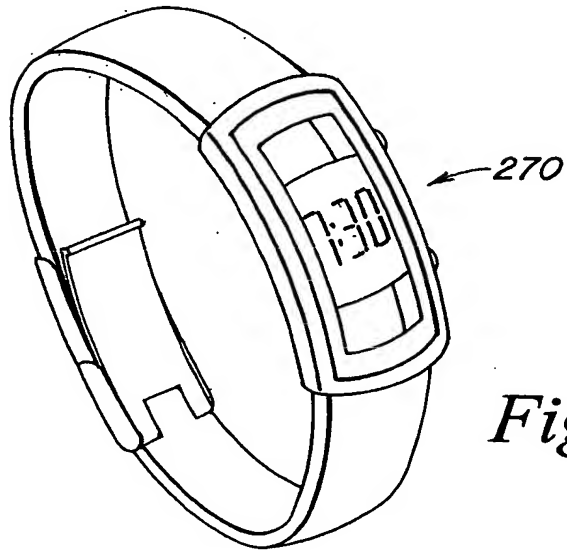


Fig. 27

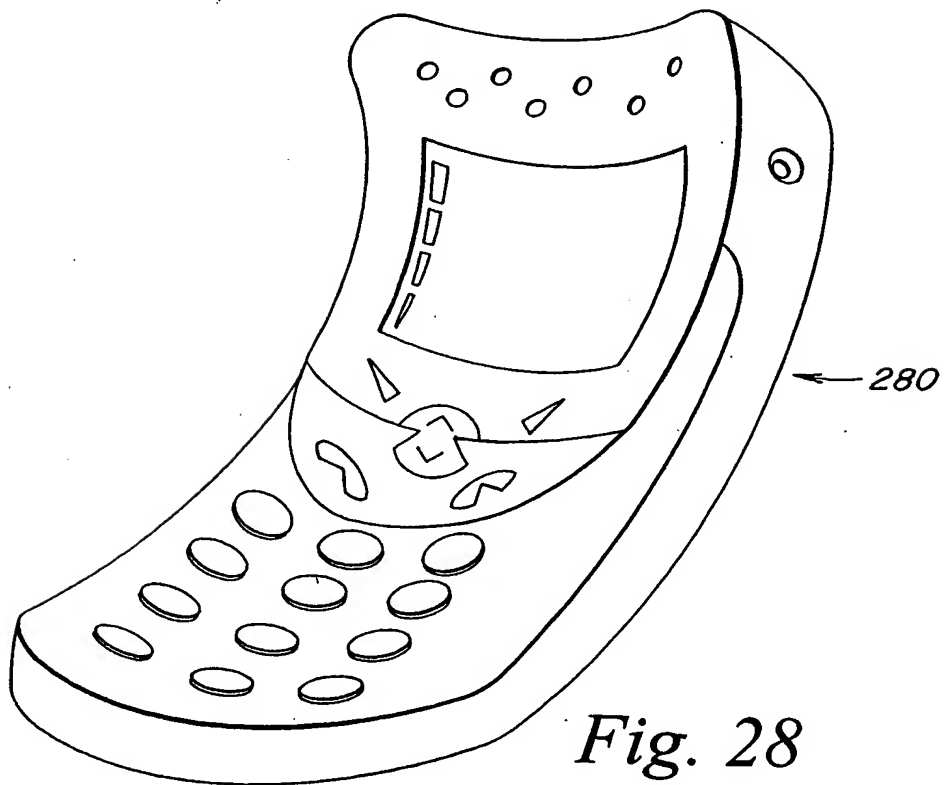


Fig. 28

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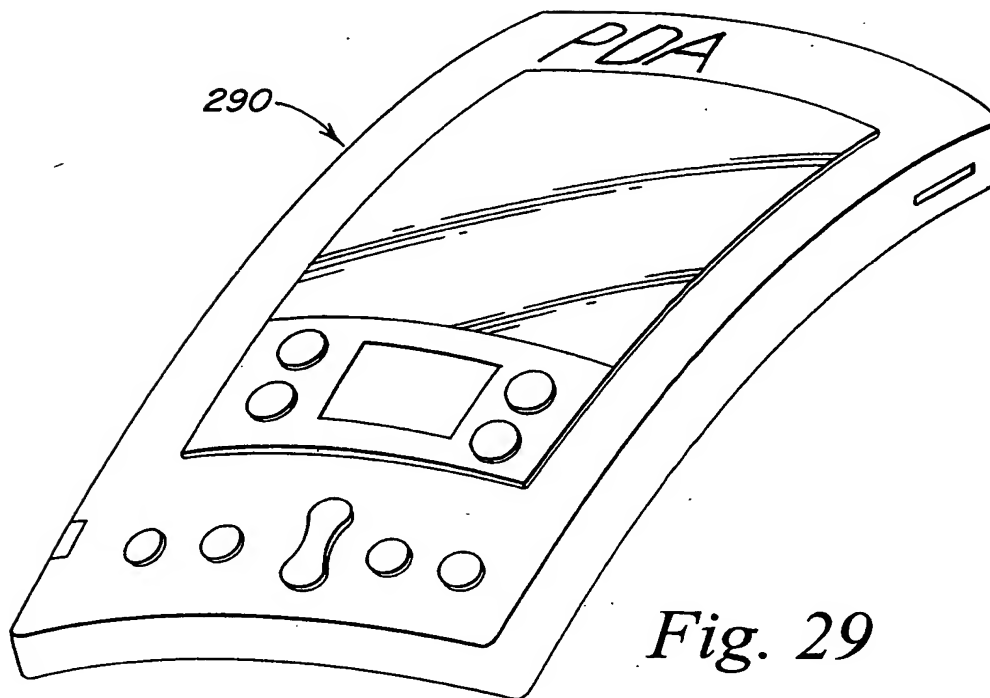


Fig. 29

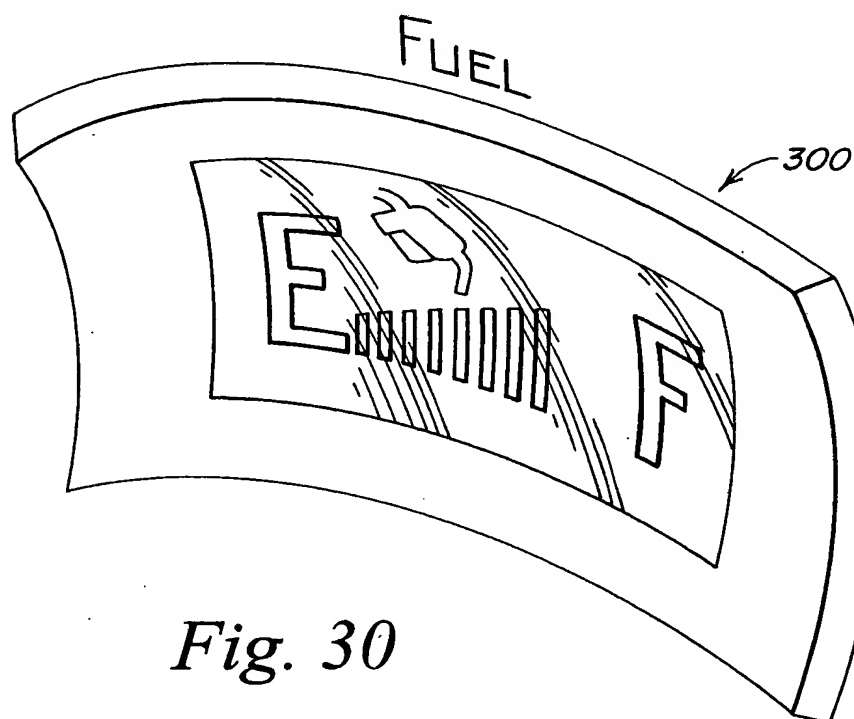


Fig. 30